

Mobin Allahdini Hasaroueiye
Master of Computer Architecture 2021

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Professional Summary

An ambitious computer architecture graduate with a special interest in hardware design. Competent in working with Xilinx Spartan FPGAs using VHDL language. Ability to work independently and as a team player.

Education

MSc in Computer Architecture (Course Base). (GPA: 15.94/20) **2019-2021**
Shahid Beheshti University; Tehran, Iran.

- **Notable Courses:**

- Advanced Computer Architecture (**Mark:** 20/20)
- Advanced VLSI System Design (**Mark:** 18/20)
- Hardware Descriptive Languages design (**Mark:** 20/20)

BSc in Hardware Engineering. (GPA:12.91/20) **2013-2018**
Khajeh Nasir Toosi University of Technology; Tehran, Iran.

- **Thesis subject:** Design and implement a module for spike sorting using self-organizing map.
Supervisor: Dr. Hossein Hosseini-Nejad
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Research Interests

- Hardware design
 - FPGAs and VHDL
 - Computer Architecture
 - Spike sorting
 - Electronic Boards and ICs
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Publications

- **Mobin Allahdini Hasaroueiye**, Dr. Hossein Hosseini-Nejad; An Online Unsupervised Spike Sorting Approach Based on Self Organizing Map. 7th Basic and Clinical Neuroscience Congress, Tehran, Iran, 2018.
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Skills

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|------------------|-------------------|---------------------------|
| • VHDL | • C# | • Html5 & Css3 |
| • Verilog | • Java | • WordPress |
| • Matlab | • ZynQ | • Photoshop |
| • C++ | • MSOffice | |
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Languages

- Persian: Native
 - English: IELTS overall band score 7.5 (L: 7.5, R: 8.5, W: 6, S: 7)
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Awards and Honors

- Achieved the ranking of 670 (Dist. 3) in Iranian's university entrance exam in 2013. (Top 2%)
 - Recognized exceptional talent by NODET.
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Projects

- **Najva:** Designing a digital board to connect a software to an RF transmitter, using UART and SPI protocols on a SPARTAN6 FPGA.
Team size: five
Language: VHDL

- **Paper Soccer:** Implementing the game on a SPARTAN3 FPGA using Q-Learning AI algorithm.
Team size: two
Language: MATLAB & VHDL
- **8-Puzzle Problem:** An AI project to solve the 8-Puzzle problem.
Team size: two
Language: c++
- **Knight Move:** A project for data structure lecture to solve Knight Move problem.
Team size: one
Language: c++

Experiences

Hardware Designer

Jan 2021 – May 2021

Did Electronic Mobin Co.

I joined the Digital team in Did Electronic Mobin Co. as a hardware designer and worked on communication interface protocols such as UART and SPI. I worked with Spartan 6 FPGAs and collaborated with RF and Software teams. I used Altium Designer to design circuit schematics.

Reasercher at FPGA Lab.

Dec 2017 – Sep 2019

K.N. Toosi University of technology

I worked on brain signals and spikes in FPGA Laboratory in KNTU. I used Matlab to simulate the software processing before hardware implementation on Spartan FPGA.

During this period, I was also the administrator of the laboratory's website (<http://fpga-lab.ee.kntu.ac.ir>).

Khatoon Abad Copper Smelter Co.

Jun 2017 – Aug 2017

University internship project

As 1st line IT support, my day-to-day responsibility was to troubleshoot hardware and software issues, maintain the network's physical layer, train and assist users on the secure use of systems and applications.

Pyramid Co.

Jun 2014 – Dec 2014

Trainee software developer

As trainee developer contributed to developing projects using C# and WPF in Visual Studio environment. I also developed the template for the company's website using HTML and CSS (www.bita24.com).

Certificates

- Participated in the 7th Basic and Clinical Neuroscience Congress in 2018.
- Participated in the 5th National Digital System Design contest of Iran in 2018
- Basic Electronics Circuits in 2007.

References

- Dr Hossein Hosseini-Nejad
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Assistant Professor K. N. Toosi University of Technology
- Dr Raziye Salarifard
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Assistant Professor Shahid Beheshti University